CLAIMS

What is claimed is:

- 1. Anapparatus, comprising:
- a data path output unit to output a packet header for a message request transaction, the packet header including a first field to indicate one of a plurality of message groups.
- 2. The apparatus of claim 1, the packet header further including a second field to indicate whether data is included with the message request transaction.
 - 3. The apparatus of claim 2, the packet header further including a message code field.
 - 4. The apparatus of claim 3, wherein the second field is a format field.
 - 5. The apparatus of claim 4, wherein the first field is a three-bit sub-field including one bit from a type field and two bits from an extended type field.
 - 6. An apparatus, comprising:
- a data path input unit to receive a packet header for a message request transaction, the packet header including a first field to indicate one of a plurality of message groups.
- 7. The apparatus of claim 6, the packet header further including a second field to indicate whether data is included with the message request transaction.

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packet header.

- 8. The apparatus of claim 7, the packet header further including a message code field.
- 9. The apparatus of claim 8, wherein the second field is a format field.
- 10. The apparatus of claim 9, wherein the first field is a three-bit sub-field including one bit from a type field and two bits from an extended type field.
 - 11. A system, comprising:
- a transmitting device to transmit a packet header for a message request transaction, the packet header including a first field to indicate one of a plurality of message groups; and a receiving device couple to the transmitting device, the receiving device to receive the
- 12. The system of claim 11, the packet header further including a second field to indicate whether data is included with the message request transaction.
 - 13. The system of claim 12, the packet header further including a message code field.
 - 14. The system of claim 13, wherein the second field is a format field.
- 15. The system of claim 14, wherein the first field is a three-bit sub-field including one bit from a type field and two bits from an extended type field.

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